

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Attorney Docket Number 50851/RJP/B600

Application Number Not Assigned

Filing Date Herewith

Applicant(s) Klaas Bult, et al.

Group Art Unit Not Assigned

Examiner Name Not Assigned

U.S. PATENT DOCUMENTS

| EXAMINER INITIALS | Cite No. ¹ | DOCUMENT NUMBER Number - kind code ² . (If known) | PUBLICATION DATE MM-DD-YYYY | NAME OF PATENTEE |
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| EXAMINER INITIALS | Cite No. ¹ | Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ⁵ (If known) | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | T ⁶ (✓) |
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OTHER DOCUMENTS

| EXAMINER INITIALS | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. |
|----------------------|--------------------------|---|
| J.L. | | K. KATTMAN, et al., "A Technique for Reducing Differential Non-Linearity Errors in Flash A/D Converters", ISSCC91/Session 10/High-Speed Data Acquisition/Paper TP 10.4, Feb. 14, 1991, pp. 170-171, 1991 IEEE International Solid-State Circuits Conference |
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R. VAN DE PLASSCHE, "Integrated Analog-to-Digital and Digital-to-Analog Converters", *The Kluwer International Series in Engineering and Computer Science; Analog Circuits and Signal Processing*, 1994, pp. 217-222, Kluwer Academic Publishers, The Netherlands

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R. VAN DE PLASSCHE, "Integrated Analog-to-Digital and Digital-to-Analog Converters", *The Kluwer International Series in Engineering and Computer Science*, 1994, pp. 148-187, Kluwer Academic Publishers, The Netherlands

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